

BOSE INSTITUTE
KOLKATA

Tender No.	CAPSS/15/39(ALICE-SD)/13-14
Tender Date	15/07/2013
Tender Type	OPEN

Tender Title	FPGA DSP Kit with High Speed Analog
Specification	<p><u>FPGA:</u></p> <ul style="list-style-type: none">• ROHS compliant evaluation kit including the XC7K325T-2FFG900C FPGA Chip <p><u>Configuration</u></p> <ul style="list-style-type: none">• Onboard JTAG configuration circuitry• 512Mb BPI FLASH for PCIe® Configuration• 16MB Quad SPI Flash• JTAG <p><u>Memory</u></p> <ul style="list-style-type: none">• SODIMM DDR3 Memory 1600Mbps• 512Mb BPI FLASH for PCIe Configuration• SD Card Slot <p><u>Communication & Networking</u></p> <ul style="list-style-type: none">• GigE Ethernet GMII, RGMII and SGMII• SFP / SFP+ cage• GTX port (TX, RX) with four SMA connectors• UART To USB Bridge• PCI Express x8 edge connector <p><u>Display</u></p> <ul style="list-style-type: none">• HDMI Video output• External Phy/codec device driving an HDMI Connector• 2x16 LCD display <p><u>Expansion Connectors</u></p> <ul style="list-style-type: none">• FMC-HPC (Partial Population) connector (4 GTX Transceiver, 116 single-ended or 58 differential (34 LA & 24 HA) user defined signals)

	<p><u>Clocking</u></p> <ul style="list-style-type: none"> • 200MHz Oscillator (Differential) • SMA Connectors for external clock (Differential) • GTX Reference Clock port with 2 SMA connectors • OBSAI/CPRI – SFP+ Received clock • EXT Config CLK <p><u>Control & I/O</u></p> <ul style="list-style-type: none"> • 5X Push Buttons • 4X DIP Switches <p><u>Power</u></p> <ul style="list-style-type: none"> • 12V wall adapter or ATX • Voltage and Current measurement capability of 2.5V, 1.5V, and 1.2V, 1.0V supplies (IIC path to FPGA) <p><u>High Speed Analog</u></p> <ul style="list-style-type: none"> • Dual channel 14-bit 250Msps A/D • Dual channel 16-bit 800Msps A/D • Supports both internal and external clocking
Quantity	ONE

Last Date & Time for submission	31/07/2013 upto 2.00 pm
Date & Time for opening bids	31/07/2013 at 3.00 p.m.
Submission of Tender (address)	CAPSS, Salt Lake, Block EN 80, Kol. 91
Venue of bid opening	CAPSS, Sale Lake, Block EN 80, Kol. 91
For any query the interested bidders may contact (Dept./Section/Div./Unit)	033 2569 3109/3123

General Terms & Conditions

Warranty	12 months from the date of satisfactory of installation
Payment terms	Payment will be made after complete delivery of the instrument in good condition and satisfactorily installation.
Delivery schedule	Within 30 days from the date of receiving order & if any defect of the supplied item is found, it should be replaced immediately from your side.
	NA

Bid security (earnest money deposit) if applicable	
Submission of Performance Bank Guarantee (PBG), if applicable	NA
Any other information (if applicable)	

Name of the instrument and submission of tender should be mentioned on the envelop positively

Director, Bose Institute reserves the right to accept or reject any or all tenders either in part or in full. The reasons for rejecting the tender of a prospective bidder will be disclosed only when enquiries are made.